

1. Junior Analog Engineer

Location: Thessaloniki, Greece

The ideal candidate will have the following skills and background:

- M.Sc.EE or equivalent degree in Electronics, Computer Science or Electrical engineering
- Highly motivated and dynamic engineer able to adopt new technology quickly
- Deep knowledge of mixed-signal design and/or analog control system design
- Deep understanding of advanced CMOS process nodes and SoC design flows to drive analog design and integration inside SoC
- Experience with tools for schematic entry, physical layout, and design verification
- Proficiency in running SPICE and Mixed-Signal simulations

The following skills will be a plus:

- Experience with RF design
- Supervision of layout and hands-on layout experience in advanced CMOS technologies
- Experience with the implementation technologies below 90nm/CMOS and all steps in analog design flow
- Knowledge in Verilog-A/AMS for analog behavioral modeling

Your key responsibilities:

- You will be part of the team working on the implementation of advanced analog IPs
- Layout, simulations and circuit design will be part of your daily tasks

We offer:

- Opportunity to work on projects for some of the best Semiconductor companies in the world
- Unique chance to join a rapidly expanding company offering fantastic career and skills development opportunities, as well as an exceptional salary
- Permanent employment and additional benefits

- Opportunity to be involved in sports and team building activities with colleagues
- Opportunity to travel abroad and work on our clients' sites

The ideal candidate will have the knowledge and the interpersonal skills to be able to lead a team of young and enthusiastic engineers working on complex product development.

For all candidates proficient knowledge of UNIX/Linux and fluent English is a prerequisite!

2. Junior ASIC Design Engineering

Location: Thessaloniki, Greece

The ideal candidate will have following skills and background:

- M.Sc.E.E or equivalent degree in Electronics or Computer Science
- Highly motivated and dynamic engineer able to adopt new technology quickly
- Knowledge of C/C++
- Knowledge of ARM CPU System Architecture
- Familiar with scripting tools and languages (e.g. bash, csh, awk, Perl).
- Familiar with software/hardware development tools (e.g. make and versioning tools (e.g. CVS/SVN)).

Knowledge of any of the following skills is big plus:

- ASIC/FPGA SoC design experience using Verilog and VHDL
- In-depth knowledge of CPU/MCU Architecture
- Experience with RTL development, synthesis, optimization and timing closure
- Experience in logic equivalence checking
- Experience in DFT/ATPG
- Experience with the implementation technologies below 90nm/CMOS
- Familiar with complex flows for System-on-Chip projects using Cadence/Synopsys/Mentor Graphics EDA tools

Responsibilities

Candidate will be involved in all stages of complex ASIC / FPGA SoC design including functional specification development, microarchitecture development, RTL design, synthesis, equivalence checking and timing sign off.

We offer:

- Comprehensive trainings for Juniors from experts for design/analog/verification of ASIC/FPGA System-on-Chip

- Unique chance to join a rapidly expanding company offering fantastic career and skills development opportunities, as well as an exceptional salary
- Permanent employment and additional benefits
- Opportunity to be involved in sports and team building activities with colleagues
- Opportunity to travel abroad and work on our clients' sites
- Working in young and enthusiastic team

Successful training completion and testing will be condition for candidates to join larger team working in all stages of complex SoC design and verification.

For all candidates proficient knowledge of UNIX/Linux and fluent English is a prerequisite!

3. Firmware Engineer

Location: Thessaloniki, Greece

The ideal candidate will have the following skills and background:

The successful candidate will have proven industry experience driving system architecture, from initial prototypes to production code.

- Master's or Bachelor's degree in Computer Science, Computer Engineering, Electrical Engineering or related technical field.
- Experience developing firmware for different microprocessors and with different tool chains including small footprint embedded systems preferred.
- Experience writing or designing firmware for UART communication interface
- Experience identifying hardware requirements based upon system firmware requirements a plus.
- Works with Engineers, Designers, Innovators, and other experts to architect and realize intent and experience design of embedded systems and solutions. Uses concept sketches, pseudo-code, engineering design/ideas, and other communication methods to assure clear understanding of technological capabilities and limitations.
- Understanding of embedded firmware architecture and system design.
- Proven strong understanding of software design processes, methodologies and approaches.
- Ability to support all phases of the product development cycle including architecture, design, development, debugging, documentation and validation.
- Experience developing firmware for systems using low power 32-bit micro-controllers.
- Experience writing or designing embedded low power management firmware.
- Demonstrated expertise in design of multitasking, event driven real time firmware using C and deep understanding of RTOS concepts.
- Excellent creativity, communication, presentation, troubleshooting, time-management and analytical skills are required.

Your key responsibilities:

You will be responsible for embedded firmware design of innovative products in AI market.

This role will assist in the development of a robust firmware platform by designing efficient, testable, and scalable code that can execute across different hardware platforms.

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4. Junior System Administrator

Location: Thessaloniki, Greece

Salary: Competitive salary

The ideal candidates should have the following skills and background:

- Bachelor's degree/MS in Computer Science, Telecommunication or related discipline
- At least B level of English language (spoken and written)
- Experience in managing Linux distros (CentOS, Ubuntu)
- Good knowledge of shell scripting (bash) and remote work via ssh
- At least basic knowledge of Python3/PHP and at least basic level of MySQL or MariaDB
- Familiarity with web servers (apache/nginx)
- Basic computer networking skills
- Highly motivated and willing and able to learn new skills and to adopt new technology quickly
- Ability to work in a team

We offer:

- Unique chance to join a rapidly expanding company offering fantastic career and skills development opportunities
- Permanent employment and additional benefits
- Opportunity to be part of many sports and teambuilding activities with colleagues
- Working in young and enthusiastic team

5. Junior Verification Engineer

Location: Thessaloniki, Greece

The ideal candidate will have following skills and background:

- M.Sc.EE or equivalent degree in Electronics, Informatics, Computer Science, Automatic, Telecommunications or Electrical engineering
- Highly motivated and dynamic engineer able to adopt new technology quickly
- Knowledge of C/C++

Additional skills that would be a plus are:

- Familiar with scripting tools and languages (e.g. bash, csh, awk, Perl).
- Familiar with software/hardware development tools (e.g. make and versioning tools (e.g. CVS/SVN).
- Knowledge of C/C++ language and/or SystemC.
- Familiar with complex flows for System-on-Chip projects.

Your key responsibilities would be:

- Involvement in all stages of complex SoC verification including specification, test-bench design, verification plan, development, verification execution and verification sign off.
- Proactive collaboration with team members in different locations
- Review of the verification environment architecture and implementation specifications.

We offer:

- For all junior engineers there will be 4-6 months technical training. It will be organized in HDL DH premises, executed by senior engineer with teaching experience from the Faculty of Electrical Engineering. The training will be organized based on Cadence (Incisive/X-Celium, Genus, Innovus, Tempus) and Mentor (QuestaSim). During the training period, the junior engineers will not have other assignments and they will have fully paid salary.
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- Opportunity to be part of many sports and teambuilding activities with colleagues
- Opportunity to travel abroad and work on our clients' sites
- Working in young and enthusiastic team

Successful training completion and testing will be condition for candidates to join larger team working in all stages of complex SoC design and verification.

For all candidates proficient knowledge of UNIX/Linux and fluent English is a prerequisite!